

UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO |
|--|-------------|----------------------|-------------------------|-----------------|
| 10/670.664 | 09/25/2003 | Albrecht Mayer | J&R-1125 | 8359 |
| 24131 75 | 09/26/2006 | | EXAM | INER |
| LERNER GREENBERG STEMER LLP | | | PUENTE, EMERSON C | |
| P O BOX 2480 HOLLYWOOD, FL 33022-2480 | | | ART UNIT | PAPER NUMBER |
| | | | 2113 | |
| | | | DATE MAILED: 09/26/2006 | |

Please find below and/or attached an Office communication concerning this application or proceeding.

| | | Application No. | Applicant(s) | |
|--|--|---|---|--|
| Office Action Summary | | 10/670,664 | MAYER, ALBRECHT | |
| | | Examiner | Art Unit | |
| | | Emerson C. Puente | 2113 | |
| Period fo | The MAILING DATE of this communication app or Reply | pears on the cover sheet with | the correspondence address | |
| A SH WHIC - Exte after - If NC - Failu Any | CORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DATES OF THE MAILING DATES OF THE MAILING DATES OF THE MONTHS from the mailing date of this communication. OF period for reply is specified above, the maximum statutory period ware to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing led patent term adjustment. See 37 CFR 1.704(b). | ATE OF THIS COMMUNIC, 36(a). In no event, however, may a repvill apply and will expire SIX (6) MONTI. cause the application to become ABA | ATION. bly be timely filed HS from the mailing date of this communication. NDONED (35 U.S.C. \$ 133) | |
| Status | | | | |
| 2a)⊠ | Responsive to communication(s) filed on 17 July This action is FINAL. 2b) This Since this application is in condition for allowar closed in accordance with the practice under E | action is non-final. | • | |
| Disposit | ion of Claims | | | |
| 5)□ 6)⊠ 7)⊠ | Claim(s) 1-21 and 23-26 is/are pending in the at 4a) Of the above claim(s) is/are withdraw Claim(s) 20,21 and 23-26 is/are allowed. Claim(s) 1-3 and 19 is/are rejected. Claim(s) 4-18 is/are objected to. Claim(s) are subject to restriction and/or | vn from consideration. | | |
| Applicati | ion Papers | | | |
| 9) <u>□</u> 10)⊠ | The specification is objected to by the Examiner The drawing(s) filed on 17 July 2006 is/are: a) Applicant may not request that any objection to the Replacement drawing sheet(s) including the correction The oath or declaration is objected to by the Example 1. | ☑ accepted or b)☐ objected arawing(s) be held in abeyanction is required if the drawing(s | e. See 37 CFR 1.85(a).) is objected to. See 37 CFR 1.121(d). | |
| Priority ι | under 35 U.S.C. § 119 | | | |
| a) | Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priori application from the International Bureau See the attached detailed Office action for a list of | s have been received. s have been received in App ity documents have been re (PCT Rule 17.2(a)). | plication No eceived in this National Stage | |
| Attachmen | | | | |
| 2) 🔲 Notic 3) 🔲 Inforr | e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date | Paper No(s)/ | mmary (PTO-413) Mail Date ormal Patent Application (PTO-152) . | |

Application/Control Number: 10/670,664

Art Unit: 2113

DETAILED ACTION

This action is made Final. Claims 1-21 and 23-26 have been examined.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-3 and 19 are rejected under 35 U.S.C. 102(e) as being anticipated by US Patent No. 6,922,794 of Tagawa et al. referred hereinafter "Tagawa".

In regards to claim 1, Tagawa discloses:

an OCDS module consisting of a plurality of components (see figure 4 and column 3 lines 19-25);

a plurality of first supply voltage connections for applying a first supply voltage (see figures 2 and 6 and column 3 lines 1-10 and column 4 lines 24-30);

a plurality of second supply voltage connections for applying a second supply voltage (see figures 2 and 6 and column 3 lines 1-10 and column 4 lines 24-30);

a plurality of additional components configured for being supplied with power by the first supply voltage (see column 3 lines 1-10);

at least some of said plurality of components of said OCDS module configured for being supplied with power by the second supply voltage (see column 3 lines 1-10).

Application/Control Number: 10/670,664 Page 3

Art Unit: 2113

a control information memory and a control device for putting said OCDS module into a state prescribed for debugging taking into account data stored in said control information memory. Tawaga discloses an in-circuit emulator comprising a emulator program, indicating a control information memory, which is read to initialize various memories and registers in the debug target registers in the debug target circuit, indicating a control device for putting said OCDS module into a state prescribed for debugging taking into account data stored in said control information memory (see figure 2 item 2 and column 3 lines 19-25).

In regards to claim 2, Tagawa discloses:

wherein said plurality of components configured for being supplied with power by the first supply voltage cannot also be supplied with power by the second supply voltage (see column 3 lines 1-10 and column 4 lines 24-30).

In regards to claim 3, Tagawa discloses:

wherein said at least some of plurality of components of said OCDS module that are configured for being supplied with power by the second supply voltage cannot also be supplied with power by the first supply voltage (see column 3 lines 1-10 and column 4 lines 24-30).

In regards to claim 19, Tagawa discloses:

wherein all of said plurality of components of said OCDS module are configured for being supplied with power by the second supply voltage (see column 3 lines 1-10 and column 5 lines 24-30).

Allowable Subject Matter

Claim 4-18 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 20,21, and 23-26 are allowed for reasons stated in the previous office action.

Response to Arguments

Applicant's arguments filed July 17, 2006 have been fully considered but they are not persuasive.

In regards to applicant's argument that "Claim 1 has been amended with significant features of allowable claim 9, which has been indicated as being allowable on page 6 of the office action," applicant has not included intervening limitations and hence the claim is not allowable. More specifically, the applicant has only added a control memory and a control device for putting the OCDS module into a state machine prescribed for debugging taking into account data stored in said control information memory. Tawaga's in-circuit emulator comprising a emulator program which is read to initialize various memories and registers in the debug target registers in the debug target circuit reads on such limitations, as indicated above. Examiner maintains his rejection.

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO

Application/Control Number: 10/670,664 Page 5

Art Unit: 2113

MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Emerson C. Puente whose telephone number is (571) 272-3652. The examiner can normally be reached on 8-5 M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert W. Beausoliel can be reached on (571) 272-3645. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

ecp

Role Al Bearsol Al